

# Mandating Memory Safety: The Time is Now

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June 3, 2021

**Many of the vulnerabilities** discovered and fixed in production code arise from highly dangerous **violations of memory safety**; these include *use-after-frees*, *buffer overflows*, and *out-of-bounds reads/writes*. Despite their more than 33-year history and the many attempts aimed at mitigating or blocking their exploitation, such vulnerabilities have remained a consistent, and sometimes worsening, threat.

- Common Weakness Enumerations (CWEs) 787, 125, 125, and 416 cover memory safety-based vulnerabilities, and these constitute four of MITRE’s top eight weaknesses of 2020, considering prevalence and severity [19].
- For the last 12 years, nearly 70% of the bugs addressed in a yearly security update in Microsoft products were memory safety bugs [6].
- In May 2020, Google reported that 70% of the serious vulnerabilities in the Chrome browser codebase catalogued since 2015 are memory-safety violations [12], and the trend has persisted since (see <https://twitter.com/LazyFishBarrel>).
- A similar vulnerability percentage has been observed in MacOS & iOS, Firefox, Ubuntu, and in other large, critical systems [11].

**Internet of Things (IOT) and other special-purpose devices are also at risk.** For example, a vulnerability in peer-to-peer communications services subjected millions cameras to attack [18]; memory-safety problems in TCP/IP stacks render millions of IOT devices at risk [2]; and routers have had critical vulnerabilities for years [14]. In general, IOT designs are often “close to the metal,” and so developers turn to C or C++. But such developers tend not to have the experience or training of those working on browsers and operating systems, which are themselves routinely patched for memory safety problems.

On May 12, 2021, the Whitehouse released its *Executive Order on Improving the Nation’s Cybersecurity* [15], which sets in motion a process to develop stronger standards for assuring the security of software procured by the federal government. If successful, these standards will increase the security of all software, not just government systems. With this order in mind:

I believe that now is the time to **mandate** that a system with even modest security requirements provide increasingly **strong evidence of its memory safety**.

What forms might this evidence take? In the best case, the evidence results from some form of **automated reasoning** – *type checking, static analysis, formal verification* – and at the least, employs automated testing.

For new development, memory safety is not hard to attain: **memory safety vulnerabilities essentially occur in only C and C++ code** [23], so the requirement can be met by programming in *any other programming language*. In the not-so-distant past, one might have reasonably argued that for certain kinds of development, popular languages such as Java and Python are not efficient enough, and C and C++ are the only reasonable choices. But within the last ten years Google has developed Go [13, 22] and Mozilla developed Rust [20] to be practical but secure alternatives to C and C++; these languages aim to be fast, low-level, *and* memory-safe. Rust and Go have been rising in popularity—IEEE’s 2019 Top Programming languages list ranks them 17 and 10, respectively, and both can be used for IOT development [16, 21].

What about active codebases already written in C and C++? Since Rust and Go are very different languages from C and C++, legacy code could be ported, perhaps incrementally, to **use language extensions such as Microsoft’s**

**Checked C**, which comprises annotations that when used pervasively will ensure spatial memory safety, ruling out out-of-bounds reads and buffer overflows [9]. Microsoft is developing Checked C to be part of their Azure Sphere IOT cloud service. Checked C is open source (built on Clang/LLVM) [7].

At Correct Computation, we are developing a tool, called **5C**, that helps developers automatically convert legacy code to Checked C [1].

Another approach is to use **static program analysis** to provide assurance that developed C code is memory safe. This is the approach being taken in FreeRTOS, a popular IOT operating system, to ensure the memory safety of its TCP library [5], and has been used in Amazon's s2n TLS library [17]. Static analysis differs from testing in an important way: While testing is able to uncover bugs, it is not able to prove their absence. Certain kinds of static analysis tools can do this because they are able to *consider all possible program executions* as part of their analysis. Even static analyzers that do not consider all executions, such as Facebook's open-source Infer tool [10], are precise enough that they would provide helpful evidence of memory safety. Running any such tools as part of a DevOps/continuous integration (CI) process is likely to increase code security [8].

At Correct Computation, we are developing a tool, called **Affix**, that allows developers to run static analyzers on projects that contain binary (native) code [?].

Barring all of the above, some useful evidence of memory safety can be provided by **automated reasoning-enhanced testing**, such as *fuzz testing* and *symbolic execution* [3, 4]. These strategies use automated intelligence to cover more execution paths than would be covered with typical testing, thereby potentially revealing dangerous, memory safety-violating vulnerabilities. As mentioned above, testing cannot guarantee the absence of bugs, so it constitutes weaker evidence than we would like. Indeed, Chrome is regularly fuzz tested, but still suffers from memory safety bugs, as mentioned at the start.

In sum: Vulnerabilities owing to a lack of memory safety have been around far too long; they constitute a significant but addressable risk. The standard we develop should mandate evidence of memory safety. We should be flexible, at least at first, about the evidence required to prove it, but we can and should **mandate that that evidence employs automated reasoning** of increasing degrees.

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